

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Setho Sing Fee and Lim Thiam Chye

**Serial No.:** 09/933,297

**Filed:** August 20, 2001

**For:** QUAD FLAT NO LEAD (QFN) GRID  
ARRAY PACKAGE, METHOD OF MAKING  
AND MEMORY MODULE AND  
COMPUTER SYSTEM INCLUDING SAME

**Examiner:** Unknown

**Group Art Unit:** 2811

**Attorney Docket No.:** 4738US (00-1113)

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail (under 37 C.F.R. § 1.8(a)) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

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1.8(a)(1)(ii)

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**LETTER TO THE CHIEF DRAFTSMAN**

Box Non-Fee Amendment  
Commissioner for Patents  
Washington, D.C. 20231

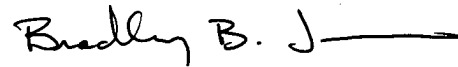
Sir:

Applicants submit herewith revised FIGS. 2 and 5 which correct errors in the drawings. Specifically, FIG. 2 has been revised to add the reference numeral 28b with appropriate lead line; and FIG. 5 has been revised to add three occurrences of reference numeral 52o with appropriate lead lines. Attached is a copy of the drawings with the proposed changes marked in red.

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No new matter has been added. Approval of the proposed revisions is respectfully requested.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Bradley B. J.", followed by a horizontal line.

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Date: November 26, 2001

BBJ/dn/blh

Enclosures: Drawings with changes marked in red